VIDEO SUPER INPOSER WITH Y-C MIXER

4.5~5.1V

GENERAL DESCRIPTION

The NJM2509 is video super imposer, including Y/C mix circuit. Y-signal input terminal have sink-chip clamp function and it is applied to fixed DC level of video signal.

Impose voltage is fixed internally to white level and black level, and includes 6dB amplifier.

■ FEATURES

- Internal Y/C Mix Circuit
- Internal Clamp Circuit (Y Signal), Bias Circuit (C Signal)
- Impose voltage fixed internally to white level and black level.
- Internal 6dB AMP. (Input:0.5VP-P, Output:1.0VPP)
- Package Outline SSOP8
- Bipolar Technology

RECOMMENDED OPERATING CONDITION

V+

Operating Voltage

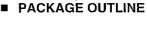
APPLICATION

Video Camera

BLOCK DIAGRAM

Yin (1 Clamp 6dB ar + GND (2 6dB amp 8) Out Cin (3 Bias Vref (7)V+ 5 6 SET (Black) MUTE CHR (White)

NJM2509V





NJM2509V

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JRC

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	· V+	7.0	v	
Power Dissipation	Рр	250	mW	
Operating Temperature Range	Topr	-20~+75	°C	
Storage Temperature Range	Tstg	-40~+125	°C	

ELECTRICAL CHARACTERISTICS

SYMBOL UNIT TEST CONDITION MIN. TYP. MAX. PARAMETER **Operating Current** lcc 5.3 7.0 8.7 mΑ Clamp Voltage V_{emp} 2.4 2.5 2.6 ν **Bias Voltage** 2.4 2.5 2.6 v Vbias Vout/Vin 100kHz, 0.5VP-P Sine Wave 6.0 6.3 6.8 dB Voltage Gain Gv $0.5V_{P-P}$ Sine Wave v_0 (10MHz)/ v_0 (100kHz) -0.7 -0.2+0.3dB Frequency Characteristic G 15.0 20.0 IRE Background Voltage From Pedestal Level 5.0 V_{set} 85.0 IRE CHR. VOLTAGE From Pedestal Level 65.0 75.0 V_{chr} Input Cin Input Resistance R_{in} 30 kΩ 0.5V_{P-P}, 10 STEP Stair wave 3.0 Differential Gain DG ----deg Differential Phasa DP 0.5V_{P-P}, 10 STEP Stair wave _ 3.0 % ____ BACKGROUND V_{eh} BACKGROUND SW:ON 2.4 v Switch Change Voltage V_{cl} BACKGROUND SW:OFF 0.8 ٧ **V**_{ch}MUTE CHRMUTE SW:ON 2.4 V CHR MUTE V_{el}MUTE CHRMUTE SW:OFF 0,8 V Switch Change Voltage CTI -50dB Crosstalk 1 Cin→BACKGROUND VOLTAGE (※1) ____ dB Crosstalk 2 CT2 C_{in}→CHR VOLTAGE (※2) -- 50 dB Crosstalk 3 CT3 Yin→BACKGROUND VOLTAGE (※1) -50 Crosstalk 4 CT4 Yin→CHR VOLTAGE (**%**2) -50 ____ dB

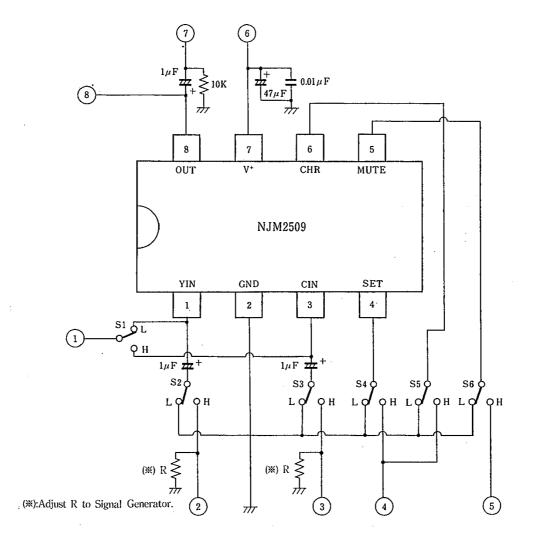
 $(V^+=4.8V, Ta=25^{\circ}C, RL=10k\Omega)$

%1. Crosstalk:4.43MHz. 0.5Vpp Sine wave, V_{out}/V_{in}

5-324-

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TEST CIRCUIT



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TERMINAL EXPLANATION

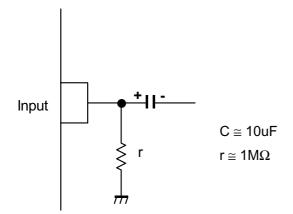
(V⁺=4.8V, Ta=25°C)

PIN NO.	UNIT	FUNCTION	EQUIVALENT CIRCUIT	PIN NO.		FUNCTION	(V*=4.8V, Ta=25C) EQUIVALENT CIRCUIT
1	YIN	Input:2.5V clamp 0.5Vpp Y-signal or Compozitto signal		5	MUTE	Ckaractor signal ON/OFF Switch Hi Charactor signal OFF Lo Charactor Signal ON	5 30k \$ 30k 26k \$ 26k
2	GND	GROUND .		6	CHR	Charactor signal Input pin Hi White level Lo Composit signal	6 19k \$ 9 k \$ 777 777
3	CIN	Input:2.5V Bias, 0.5Vpp C-signal	V ⁺ 30k 500 70/4 F	7	V+	Supply Voltage	
4	SET	Charactor signal Input Pin Hi Black level Lo Composit signal	(4) 19k 9 k 777 777	8	OUT	Output-IVpp Composit signal, Impose Voltage	V ⁺

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■APPLICATION

This IC requires 1MΩ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



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